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APPLICANTS Katsuya Fukase, Nagano, JAPAN; Toyoaki Sakai, Nagano, JAPAN; Toyoaki Sakai, Nagano, JAPAN; Munetoshi Irisawa, Tokyo, JAPAN; Toyokazu Komuro, Tokyo, JAPAN; Yasuo Kaneda, Tokyo, JAPAN; Masanori Natsuka, Tokyo, JAPAN; Wakana Aizawa, Tokyo, JAPAN; Wakana Aizawa, Tokyo, JAPAN; **CONTINUING DATA*** This application is a 371 of PCTJIPO5/03490 03/02/2005 **FOREIGN APPLICATIONS** JAPAN 2004-059832 03/03/2004 JAPAN 2004-059832 03/03/2004 JAPAN 2004-059831 21/15/2004 JAPAN 2004-382994 12/15/2004 JAPAN 2004-059829 03/03/2004 JAPAN 2004-059827 03/03/2004 JAPAN 2004-059827 03/03/2004 JAPAN 2004-059828 03/03/2004 JAPAN 2004-059828 03/03/2004 JAPAN 2004-059829 03/03/2004 JAPAN 2004-059821 03/03/2004									
JAPAN 2004-362996 12/15/2004 JAPAN 2004-362998 12/15/2004 JAPAN 2004-362997 12/15/2004 JAPAN 2004-059635 03/03/2004 JAPAN 2004-362991 12/15/2004									
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TITLE									
Circuit board manufacturing method and circuit board									
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